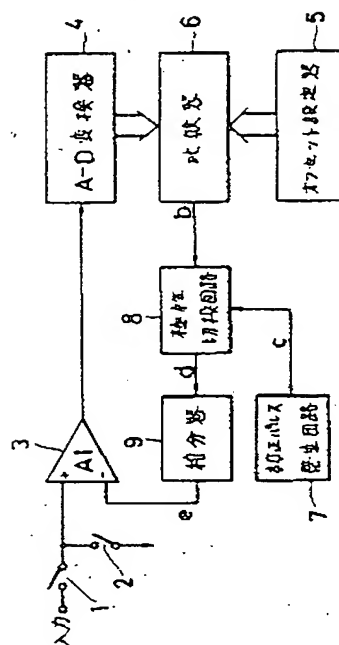


Patent Abstracts of Japan

TITLE : OFFSET/DRIFT CORRECTING
CIRCUIT FOR ANALOG-DIGITAL
CONVERTER



CONSTITUTION: A voltage difference between an analog voltage and a correction voltage given via switches 1, 2 is amplified 3, A/D-converted 4 sequentially and a desired offset value is set 5. A digital output value of the converter 4 is compared 6 with the offset setting value at the correction mode and a signal for polarity selection is outputted. Every time the converter 4 conducts A/D conversion, a correction pulse corresponding to analog quantity corresponding to the least significant 1 bit of the converter 4 is generated 7. The polarity of the pulse is selected by a signal of a polarity changeover circuit 8, an integration circuit 9 integrates and holds the correction pulse and outputs it as a correction voltage.

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